



FIG. 1A

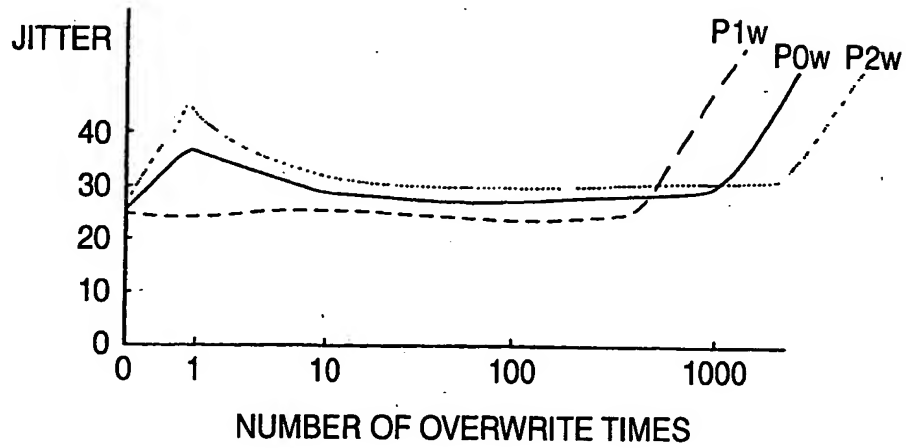
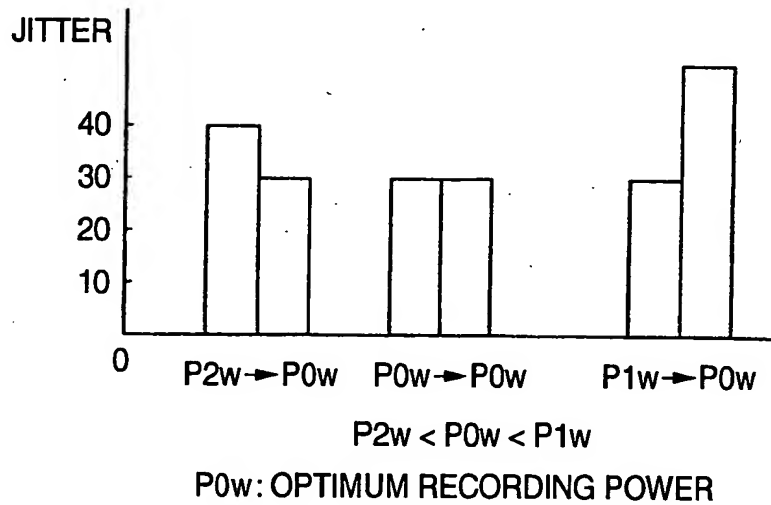


FIG. 1B





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FIG. 2A

RECORDING POWER P_{2w}

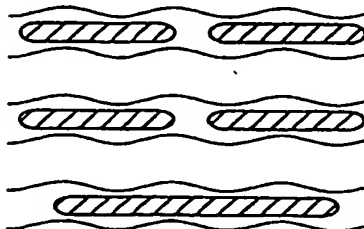


FIG. 2B

RECORDING POWER P_{0w}

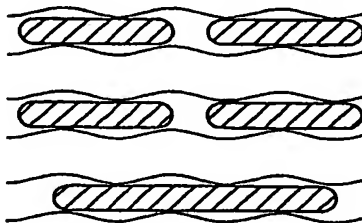
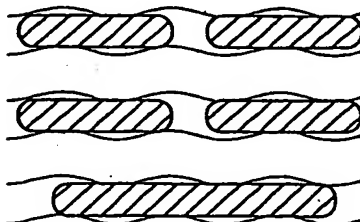


FIG. 2C

RECORDING POWER P_{1w}



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FIG. 3A

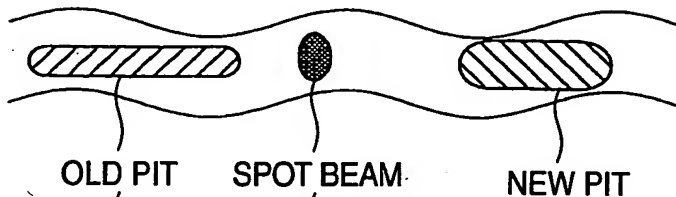


FIG. 3B

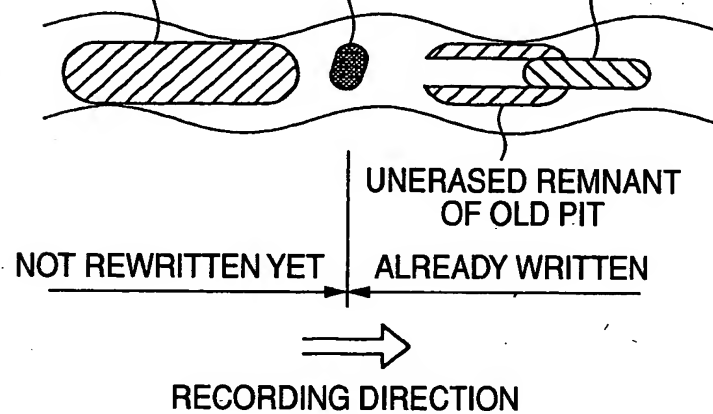


FIG. 4

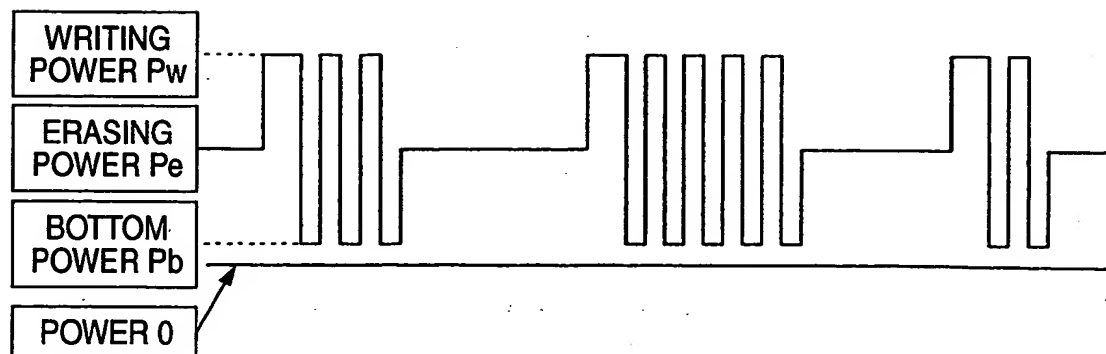


FIG. 5A

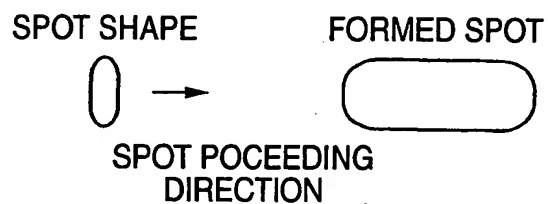


FIG. 5B



FIG. 5C

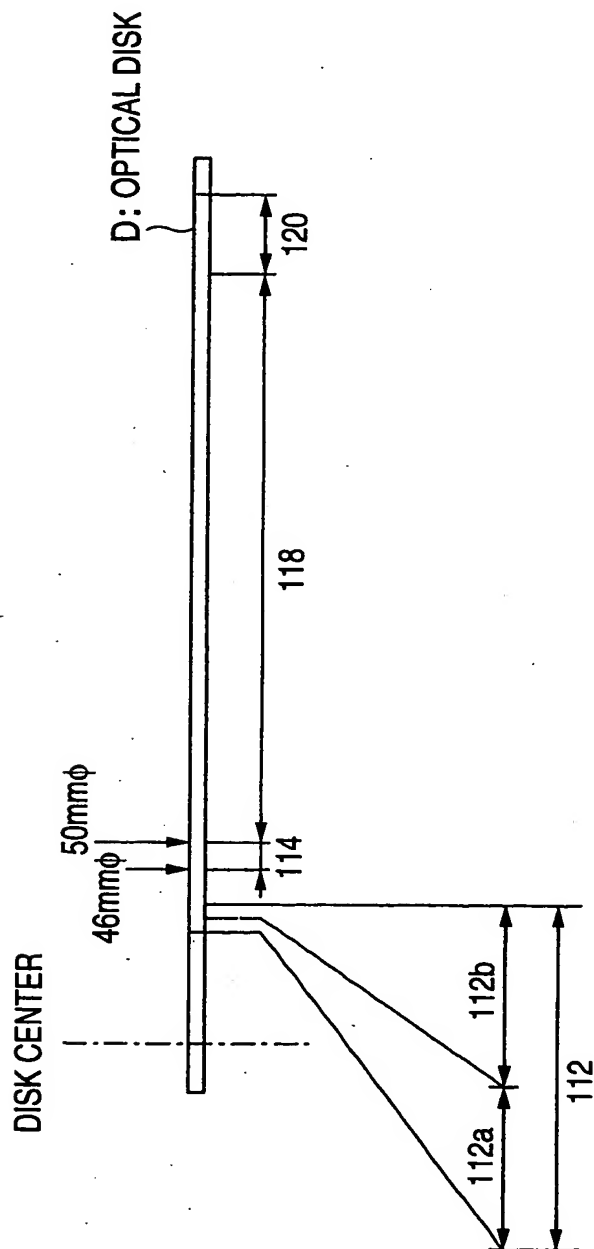


FIG. 5D



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FIG. 7



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FIG. 8A

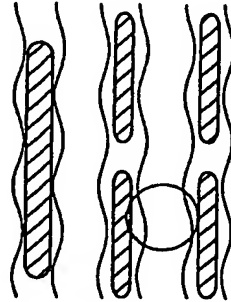


FIG. 8B

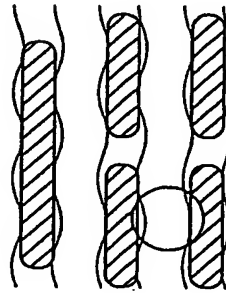
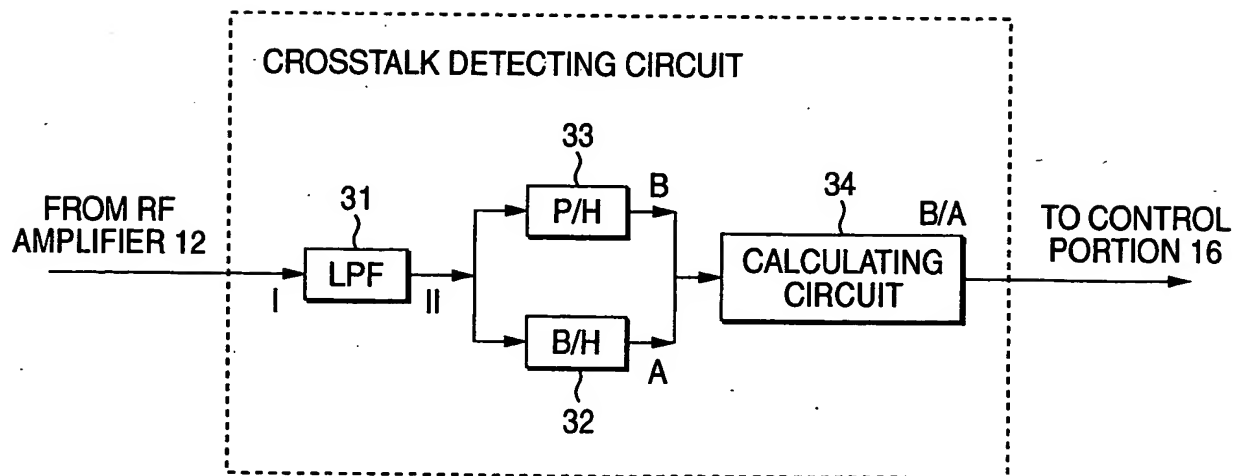


FIG. 9



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FIG. 10A

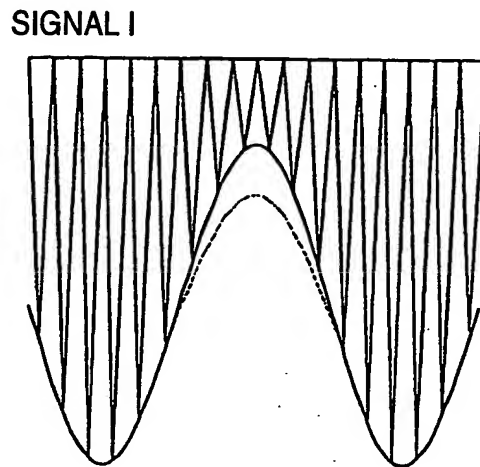
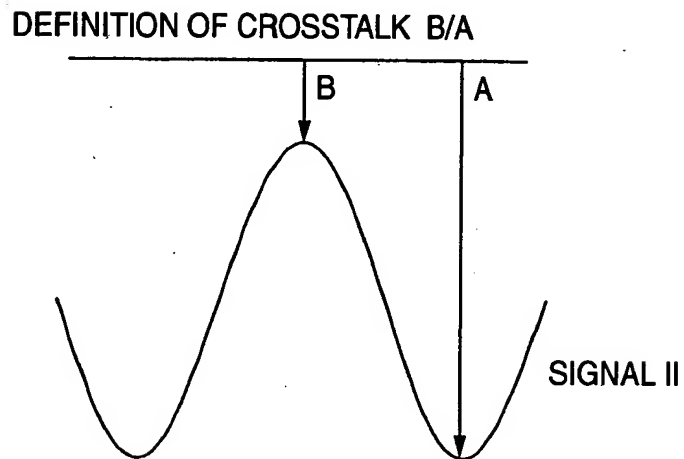
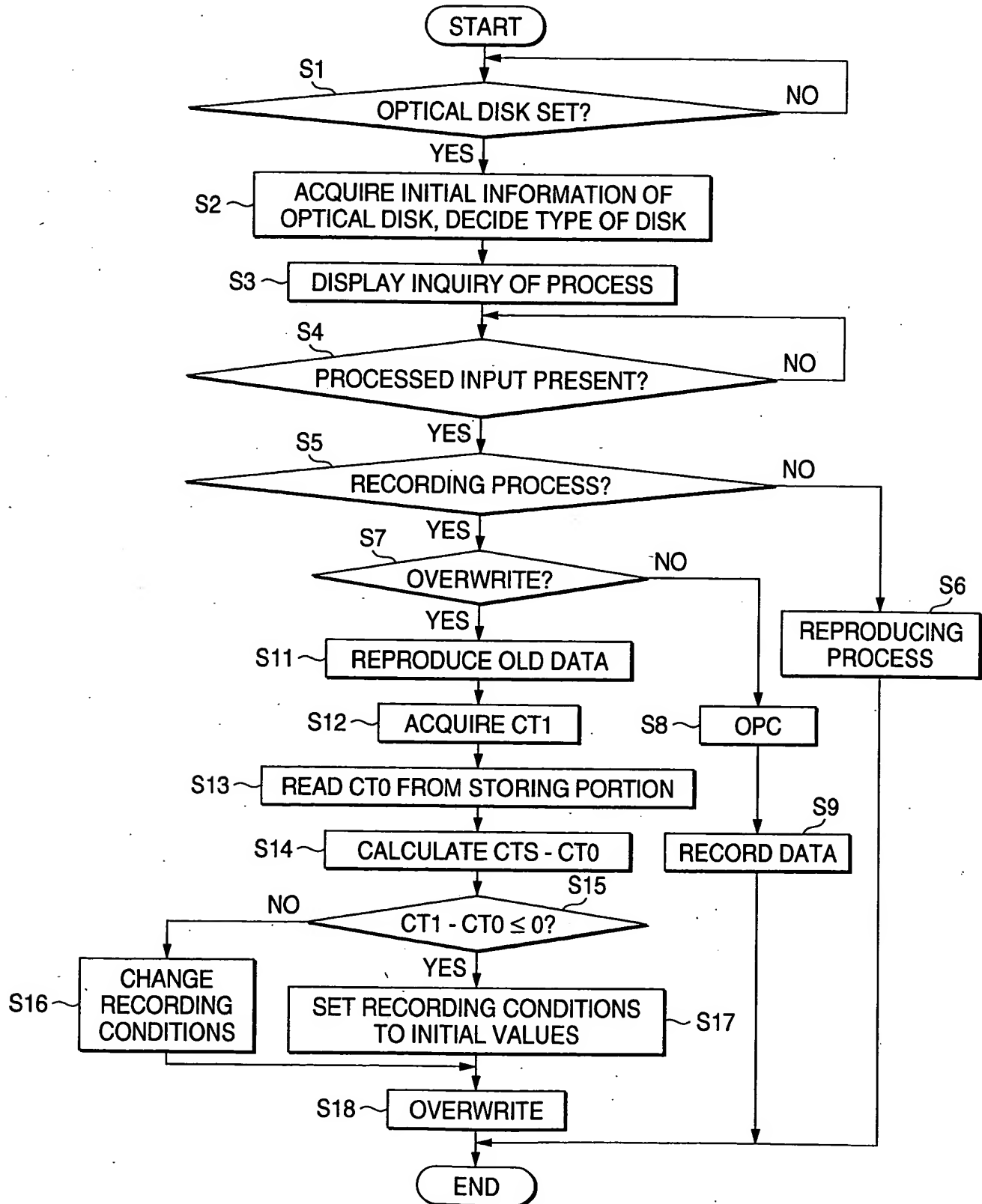


FIG. 10B



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FIG. 11



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FIG. 12A

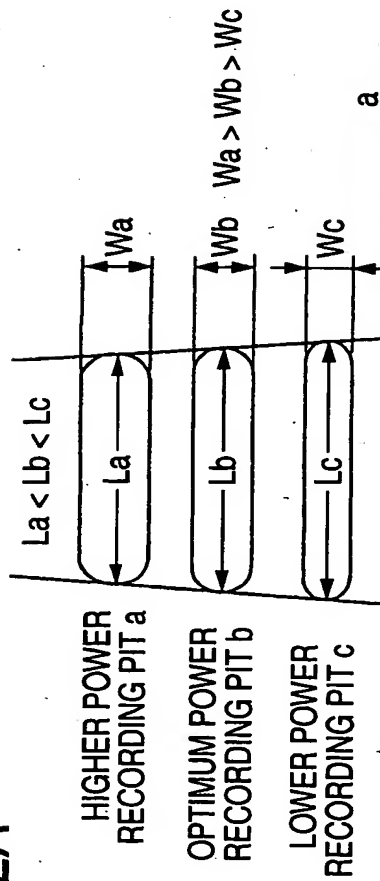
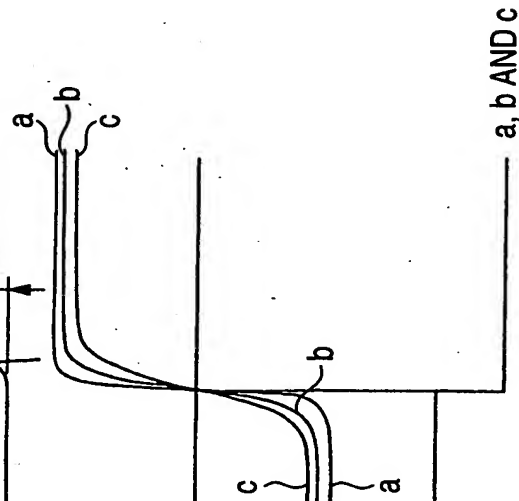


FIG. 12B

REPRODUCED WAVEFORM
 (AFTER PASSED THROUGH
 AC COUPLER)

FIG. 12C

BINARY-CODED WAVEFORM



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FIG. 13C

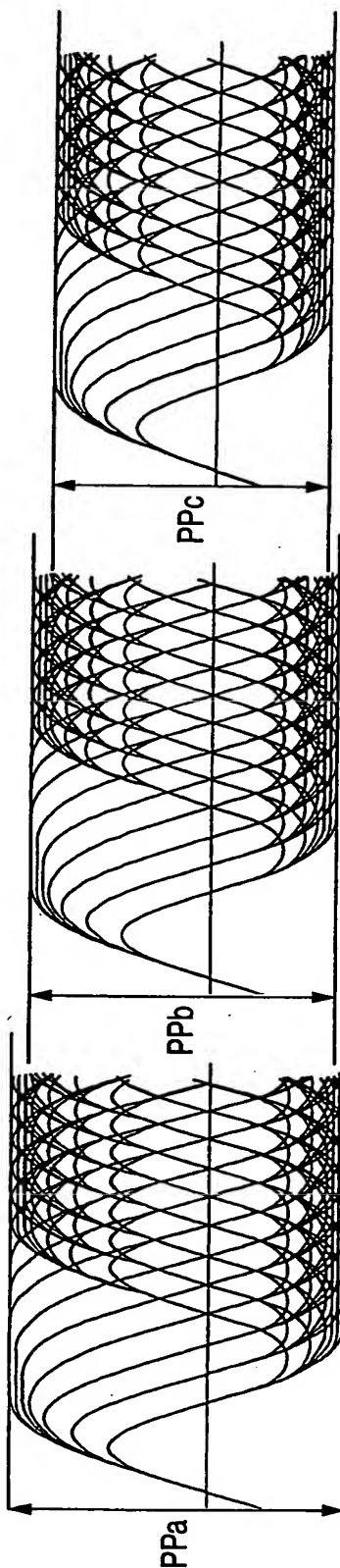
LOWER POWER RECORDING
EYE PATTERN

FIG. 13B

OPTIMUM POWER RECORDING
EYE PATTERN

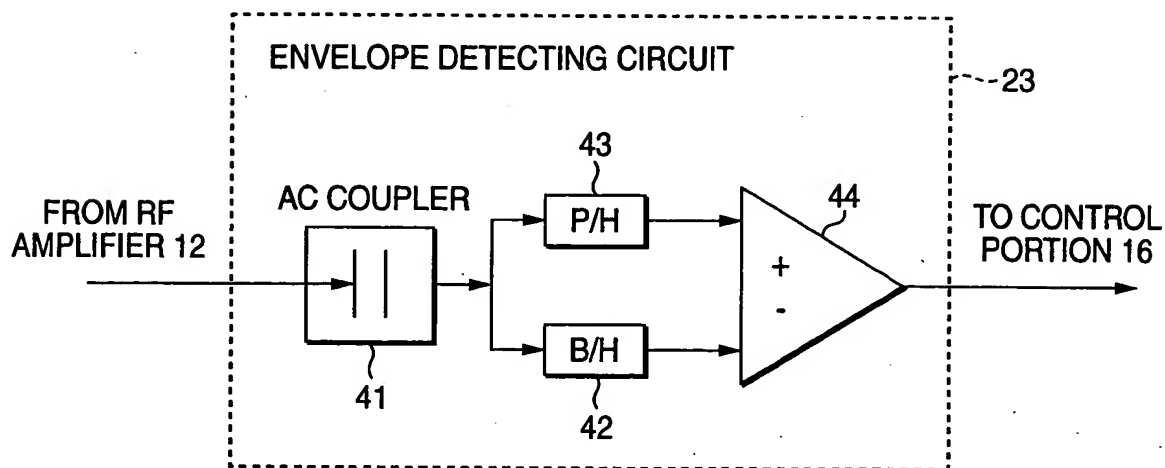
FIG. 13A

HIGHER POWER RECORDING
EYE PATTERN



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FIG. 14



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FIG. 15

